

RONI CHAIA

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OBJECTIVE To obtain a design and development position in hardware engineering, where I can use my education and two years experience. Willing to relocate.

EDUCATION **BASc Electrical Engineering**, 2004 (Graduated with Honors, **GPA: 3.6/4.0**)
University of Waterloo, Waterloo, ON, Canada

Relevant Courses:

- ◆ Filter Design
- ◆ Electronic Circuits
- ◆ RTL Digital Design
- ◆ Communication Systems
- ◆ Signals and Systems
- ◆ Computer Architecture
- ◆ Digital Communications
- ◆ Communication Networks
- ◆ Analog Control Systems
- ◆ Data Structures
- ◆ Electromagnetic Waves
- ◆ Management Science

SKILLS

Hardware ◆ Equipment: Agilent/Rohde&Schwarz (spectrum analyzer, base station emulator, scope)
◆ Experience in board level design and characterization for a wireless application (BlackBerry)

Software ◆ Tools: Cadence (Capture, PSpice), LabVIEW, Agilent VEE, LabWindows/CVI, AutoCAD
◆ Altera and Xilinx FPGAs, VHDL, Assembly
◆ Languages: C/C++, Java (**Sun Certified Programmer**), Matlab, Visual Basic, SQL
◆ Packages: Excel, PowerPoint, Visio, QXDM, QPST, MS SQL Server, Crystal Reports

Writing ◆ Extensive experience in technical writing, documentation, and presentation

EXPERIENCE **Polytronics Engineering**, *Hardware Engineer*, Nov 2005-Jan 2006, Toronto, Ontario
◆ Prepared bill of materials, ordered parts for circuit boards, tested and debugged various hardware

Solectron Invotronics, *Electronics Test Engineer*, Aug-Dec 2003, Toronto, Ontario
◆ Designed and implemented PLC based testers to automate functional verification of circuit boards
◆ Added new features to assembly line testers using C in LabWindows/CVI environment
◆ Wrote several technical manuals and operating procedures for hardware and software systems

Research in Motion, *Hardware Design Engineer*, Jan-Aug 2002, Ottawa, Ontario
◆ Improved BlackBerry battery life by making hardware changes that eliminated current leakage
◆ Evaluated the power management chip for the next generation BlackBerry by thoroughly testing its low drop-out (LDO) regulators, and analyzing its battery charging profile for optimal charging time
◆ Improved the performance of the BlackBerry by optimizing its Read and Write timing parameters
◆ Participated in designing a hardware/software system for drive-testing the BlackBerry
◆ Gained experience in soldering size 0402 components on RF circuit boards
◆ Assisted in the design of a filter to reduce cross talk between the digital and RF signals

Greater Toronto Airports Authority, *Network Engineer*, Apr-Aug 2001, Toronto, Ontario
◆ Conducted research on MPLS, DWDM, VoIP, Call Centers, PBX and Unified Messaging

Visteon Corporation, *Software Developer*, Sep-Dec 2000, Toronto, Ontario
◆ Implemented a client/server application using Visual Basic and Microsoft SQL Server

DANA Corporation, *Manufacturing Engineer*, Jan-May 2000, Cambridge, Ontario
◆ Solved manufacturing problems, designed mechanical fixtures using AutoCAD, and dealt with suppliers

INTERESTS ◆ **IEEE member**
◆ Tennis, volleyball (team captain for two years), soccer, swimming, billiards, table tennis
◆ Top-notch chef, enjoy creating healthy and flavorful foods

REFERENCES Available upon request